

### **ABSTRACT**

A circuit for controlling two switches assembled in anti-parallel, comprising in series between two terminals of the anti-parallel assembly, two identical control stages respectively dedicated to each switch and between which is interposed a common impedance setting a phase angle for the turning-on of the switches, each stage comprising: a controllable current source for providing a current to a control electrode of the concerned switch; a capacitor for storing a supply voltage of at least the current source; an element of activation/deactivation of the current source according to the voltage across the stage capacitor; and an assembly for discharging the capacitor.

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